

ECE 560 @ 11:45 in 2207 EB3	ECE 460 @ 11:45 in 2015 EB1	ECE 560 @ 11:45 in 2207 EB3	ECE 460 @ 11:45 in 2015 EB1	F
Monday	Tuesday	Wednesday	Thursday	
8/18/2025 1: Course Overview, Start Key Concepts (L1): Inputs and Outputs, Control Systems, Timing Requirements, Processes in SW and HW, Process Interactions, Timing Analysis, Target Platform	8/19/2025 1: Course Overview, Start Key Concepts (L1): Inputs and Outputs, Control Systems, Timing Requirements, Processes in SW and HW, Process Interactions, Timing Analysis, Target Platform	8/20/2025 2: Complete Key Concepts (L1). Level 2 Introduction.	8/21/2025 2: Complete Key Concepts (L1). Level 2 Introduction.	8/22/2025
8/25/2025 3: Blinky Arch. (L2): Basics, Digital & Analog I/O	8/26/2025 3: Blinky Arch. (L2): Basics, Digital & Analog I/O	8/27/2025 4: WaveGen Arch. (L2): Stabilizing Timing	8/28/2025 4: WaveGen Arch. (L2): Stabilizing Timing	8/29/2025 THW1 Due: Software, Hardware, Time and MegaHertz.
9/1/2025 Labor Day	9/2/2025 5: Scope Arch. (L2): Synchronizing Processes (do and don't)	9/3/2025 5: Scope Arch. (L2): Synchronizing Processes (do and don't)	9/4/2025 6: Expansion Shield Arch. (L2): Buck converter, touch screen, SPI and Serial	9/5/2025 PHW1 Due: Get to Know the Toolchain
9/8/2025 6: Expansion Shield Arch. (L2): Buck converter, touch screen, SPI and Serial	9/9/2025 7: Design Process and Extracting Architectures	9/10/2025 7: Design Process and Extracting Architectures	9/11/2025 8: Debugging Embedded Systems	9/12/2025 THW2 Due: Leveraging Peripherals and Sched/Sync/Comm Support
9/15/2025 8: Debugging Embedded Systems	9/16/2025 Wellness Day	9/17/2025 9: Engineering Processes for Embedded System Development	9/18/2025 9: Engineering Processes for Embedded System Development	9/19/2025
9/22/2025 10: Blinky Implementation (L3): Peripherals (GPIO, CMP, DAC, ADC, TPM), software, and interactions	9/23/2025 10: Blinky Implementation (L3): Peripherals (GPIO, CMP, DAC, ADC, TPM), software, and interactions	9/24/2025 11: WaveGen Impl. (L3): Peripherals (DAC, TPM, DMA), software and interactions	9/25/2025 11: WaveGen Impl. (L3): Peripherals (DAC, TPM, DMA), software and interactions	9/26/2025 PHW2 Due: System Analysis and General Debugging
9/29/2025 12: Scope Impl. (L3): Peripherals (GPIO, TPM, DMA, LCD Ctr), software and interactions	9/30/2025 12: Scope Impl. (L3): Peripherals (GPIO, TPM, DMA, LCD Ctr), software and interactions	10/1/2025 13: Shield Impl. (L3): Control systems, buck converter constant-current LED driver	10/2/2025 13: Shield Impl. (L3): Control systems, buck converter constant-current LED driver	10/3/2025 THW3 Due: Using HW and SW Wisely
10/6/2025 14: Buffer Day	10/7/2025 14: Buffer Day	10/8/2025 15: Midterm Exam	10/9/2025 15: Midterm Exam	10/10/2025
10/13/2025 Fall Break	10/14/2025 Fall Break	10/15/2025 16: More Peripherals: serial communications, robustness	10/16/2025 16: More Peripherals: serial communications, robustness	10/17/2025
10/20/2025 17: Better Scheduling and Architecture, no preemption (L4 Intro)	10/21/2025 17: Better Scheduling and Architecture, no preemption (L4 Intro)	10/22/2025 18: Better Sched. and Arch. for Blinky, WaveGen and Scope (L4)	10/23/2025 18: Better Sched. and Arch. for Blinky, WaveGen and Scope (L4)	10/24/2025 PHW3 Due: Evaluating Mixed-Signal Closed-Loop Embedded Systems
10/27/2025 19: Better Sched. and Arch. for Shield (L4)	10/28/2025 19: Better Sched. and Arch. for Shield (L4)	10/29/2025 20: Even Better Sched. and Arch. with a Preemptive RTOS and its Services (L5)	10/30/2025 20: Even Better Sched. and Arch. with a Preemptive RTOS and its Services (L5)	10/31/2025 THW4 Due: Scheduling, Using RTOS services
11/3/2025 21: Even Better Sched. and Arch. for Blinky, WaveGen and Scope (L5)	11/4/2025 21: Even Better Sched. and Arch. for Blinky, WaveGen and Scope (L5)	11/5/2025 22: Even Better Sched. and Arch. for Shield (L5)	11/6/2025 22: Even Better Sched. and Arch. for Shield (L5)	11/7/2025
11/10/2025 23: More RTOS Features and Methods (L5)	11/11/2025 23: More RTOS Features and Methods (L5)	11/12/2025 24: Buffer Day	11/13/2025 24: Buffer Day	11/14/2025 FP.1 Due: Shields Up! Synchronization
11/17/2025 25: Dependable Systems 1	11/18/2025 25: Dependable Systems 1	11/19/2025 26: Dependable Systems 2	11/20/2025 26: Dependable Systems 2	11/21/2025
11/24/2025 27: Buffer Day	11/25/2025 27: Buffer Day	11/26/2025 Thanksgiving	11/27/2025 Thanksgiving	11/28/2025 Thanksgiving
12/1/2025 28: Buffer Day	12/2/2025 28: Buffer Day, FP.2 Due	12/3/2025 Reading Day	12/4/2025 460 Final Exam	12/5/2025 560 Final Exam
12/8/2025	12/9/2025	12/10/2025	12/11/2025	12/12/2025 Grades Due